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APPLICATION NO	). F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,167		09/12/2003	Dureseti Chidambarrao	FIS920030185US1	2166
29625	7590	10/19/2004		EXAMINER	
	E WOOD:		PERKINS, PAMELA E		
SUITE 1800				ART UNIT	PAPER NUMBER
MCLEAN, VA 22102-4215				2822	
				DATE MAILED: 10/19/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Consumer		10/605,167	CHIDAMBARRAO ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Pamela E Perkins	2822				
Period fo	The MAILING DATE of this communication or Reply	appears on the cover sheet with	the correspondence address				
THE - Exte after - If the - If NC - Failt Any	MAILING DATE OF THIS COMMUNICATIOnsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. The period for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory per ure to reply within the set or extended period for reply will, by stareply received by the Office later than three months after the may be a patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply reply within the statutory minimum of thirty (3 iod will apply and will expire SIX (6) MONTHS tute, cause the application to become ABANI	be timely filed  0) days will be considered timely.  S from the mailing date of this communication.  DONED (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on 22	<u> 2 July 2004</u> .					
		his action is non-final.					
3)	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)🖂	☑ Claim(s) <u>1-18</u> is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠	Claim(s) <u>14-18</u> is/are allowed.						
6)⊠	Claim(s) <u>1-6</u> is/are rejected.						
7)⊠	Claim(s) <u>7-13</u> is/are objected to.						
8)	Claim(s) are subject to restriction and	d/or election requirement.					
Applicat	ion Papers						
9)[	The specification is objected to by the Exam	iner.					
10)🛛	☑ The drawing(s) filed on <u>12 September 2003</u> is/are: a)☑ accepted or b)☐ objected to by the Examiner.						
	Applicant may not request that any objection to t	he drawing(s) be held in abeyance.	See 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the corr	rection is required if the drawing(s)	is objected to. See 37 CFR 1.121(d).				
11)	The oath or declaration is objected to by the	Examiner. Note the attached O	ffice Action or form PTO-152.				
Priority (	ınder 35 U.S.C. § 119						
12)	Acknowledgment is made of a claim for forei	ian priority under 35 H.S.C. & 11	19(a)-(d) or (f)				
	☐ All b)☐ Some * c)☐ None of:	ight phonty under 55 C.C.S. § 11	19(4)-(0) 01 (1).				
_/	1. Certified copies of the priority docume	ents have been received					
	2. Certified copies of the priority docume		lication No				
	3. Copies of the certified copies of the p						
	application from the International Bure						
* 5	See the attached detailed Office action for a l		ceived.				
Attachmen		_					
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)		mary (PTO-413) lail Date				
3) 🔯 Infori	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/		mal Patent Application (PTO-152)				
Pape	r No(s)/Mail Date <del>7/22/04</del> . 6 ) 8 M	6)  Other:	. ,				

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### **DETAILED ACTION**

This office action is in response to the filing of the request for reconsideration on 22 July 2004. Claims 1-18 are pending.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Doyle et al. (6,228,694).

Doyle et al. disclose a method for manufacturing a semiconductor device where a semiconductor layer is formed on a substrate; forming an oxide layer between the semiconductor layer and the substrate; expanding a first region of the substrate to push up a first portion of the semiconductor layer; compressing a second region of the substrate to pull down a second portion of the semiconductor layer; forming an N type device over the first portion of the semiconductor layer; and forming a P type device over the second portion of the semiconductor layer (Fig. 2A & 2B; col. 3, lines 27-63; col. 5, lines 5-25). Doyle et al. further disclose the step of expanding the first region comprises a step of ion-implanting an expansion element in the first region of the substrate, wherein the expansion element is ion-implanted at an implantation concentration of approximately 1 x 10<sup>14</sup> atoms/cm<sup>2</sup> and 5 x 10<sup>16</sup> atoms/cm<sup>2</sup> to at an

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implantation energy of approximately 30 KeV to 300 KeV and the concentration peak of the implanted expansion element is confined within the first region (col. 8, lines 4-56). Doyle et al. also disclose the expansion element as  $0_2$  (col. 8, lines 17-20).

## Allowable Subject Matter

Claims 7-13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 14-18 are allowed.

The following is a statement of reasons for the indication of allowable subject matter: prior art does not anticipate, teach, or suggest a method of manufacturing a semiconductor device where a semiconductor layer is formed on a substrate; selectively ion-implanting an expansion element in a first region of the substrate; selectively ion-implanting a compression element in a second region of the substrate; annealing to expand the first region and to compress the second region, wherein the expanded first region pushes up a first portion of the semiconductor layer and the compressed second region pulls down a second portion of the semiconductor layer; and forming an N type device on the first portion of the semiconductor layer; and forming a P type device on the second portion of the semiconductor layer.

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## Response to Arguments

Applicant's arguments filed 22 July 2004 have been fully considered but they are not persuasive. As stated above, Doyle et al. disclose the method of manufacturing a semiconductor device as described in claims 1-6.

In response to the applicant's arguments, the applicant argues prior art does not teach expanding or compressing any region in a substrate. However, Doyle et al. does disclose applying a tensile to expand a region of a substrate and applying a compressive stress to another region of the substrate (Fig. 2A &2B; col. 3, lines 27-44). Applicant also argues prior art teaches forming voids in the substrate. However, claim 1 does not disclose the technique used to expand or compress regions of the substrate.

### Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pamela E Perkins whose telephone number is (571) 272-1840. The examiner can normally be reached on Monday thru Friday, 9:00am to 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PEP

AMIR ZARABIAN
SUPERVISORY PATENT EXAMINER

TOHNOLOGY CENTER 2800